

ABSTRACT OF THE DISCLOSURE

In a plasma display device having a reduced discharge-current-induced voltage fluctuation and an expanded drive margin and being successful in preventing the display characteristics from being degraded, a Y-electrode drive circuit and an X-electrode drive circuit for supplying a drive voltage to the capacitance which represents a display cell are configured using parallel circuits in which first switching elements having a high-speed-switching performance and second switching elements having a low-saturation-voltage performance are connected in parallel, so that the second switching elements having the low-saturation-voltage performance are turned on at least during a period that discharge current flows therebetween.